

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L4	28	((cpu processor) near3 frequency near3 higher) near5 (memory RAM DRAM SDRAM)	USPAT	OR	OFF	2006/06/01 14:20
L5	4	("5383161" "5432468" "5485589" "5491814").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/06/01 14:36
L6	157	(cpu processor) with (memor\$3 RAM\$1 DRAM\$1 SDRAM\$1) with ((frequency speed) near3 different)	US-PGPUB; USPAT; EPO	OR	OFF	2006/06/01 14:37
L7	50	("4527145" "5099477").PN. OR ("5448715").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/06/01 16:23
L8	1	"20040230855".pn.	US-PGPUB	OR	OFF	2006/06/01 16:29
L9	75245	rate near3 (convert\$3 adjust\$3)	US-PGPUB; USPAT; EPO	OR	OFF	2006/06/01 16:36
L10	9223	((send\$3 writ\$3) near3 data) near5 (processor cpu) near4 (memory RAM SDRAM DRAM)	US-PGPUB; USPAT; EPO	OR	OFF	2006/06/01 16:37
L11	6	9 with 10	US-PGPUB; USPAT; EPO	OR	OFF	2006/06/01 16:38
L12	10	9 same 10	US-PGPUB; USPAT; EPO	OR	OFF	2006/06/01 16:38
L13	4	12 not 11	US-PGPUB; USPAT; EPO	OR	OFF	2006/06/01 16:38
L14	5	(cpu processor) near5 memory near5 (domain\$1 near3 different)	US-PGPUB; USPAT; EPO	OR	OFF	2006/06/01 16:39
L15	55928	memory adj (interface controller)	US-PGPUB; USPAT; EPO	OR	OFF	2006/06/01 16:43
L16	3	15 with (domain near3 (cross\$3 translat\$3))	US-PGPUB; USPAT; EPO	OR	OFF	2006/06/01 16:46
L17	1993	memory near3 paging	US-PGPUB; USPAT; EPO	OR	OFF	2006/06/01 17:46
L18	0	17 with (minimiz\$3 near3 access\$3)	US-PGPUB; USPAT; EPO	OR	OFF	2006/06/01 17:47

EAST Search History

S1	11644	((data adj rate) bandwidth) near3 memory	US-PGPUB; USPAT; EPO	OR	OFF	2006/04/25 17:50
S2	210	S1 with (convert\$3 adjust\$3)	US-PGPUB; USPAT; EPO	OR	OFF	2006/04/25 17:50
S3	1583	(713/400 710/29).ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/04/25 17:51
S4	1	S2 and S3	US-PGPUB; USPAT; EPO	OR	OFF	2006/04/25 17:51
S5	92	S1 and S3	US-PGPUB; USPAT; EPO	OR	OFF	2006/04/25 17:56
S6	0	((cpu processor) near3 "faster than") with (memory RAM SDRAM)	US-PGPUB; USPAT; EPO	OR	OFF	2006/04/25 17:57
S7	0	713/29.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/04/25 17:57
S8	412	710/29.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/04/25 17:57
S9	169	S8 and (memory near3 (interface controller))	US-PGPUB; USPAT; EPO	OR	OFF	2006/04/25 17:57
S10	137	S8 and (memory near3 controller)	US-PGPUB; USPAT; EPO	OR	OFF	2006/04/25 17:58
S11	80	S8 and (memory adj controller)	US-PGPUB; USPAT; EPO	OR	OFF	2006/04/25 18:06
S12	13	(memory near3 interfac\$3) with ((processor bus) near3 faster)	US-PGPUB; USPAT; EPO	OR	OFF	2006/04/25 18:10
S13	17	((memory near3 interfac\$3) near3 slower) with (processor bus)	US-PGPUB; USPAT; EPO	OR	OFF	2006/04/25 18:13
S14	262	(memory adj (interface controller)) near5 asynchronous\$2	US-PGPUB; USPAT; EPO	OR	OFF	2006/04/25 18:13
S15	157	(memory adj (interface controller)) near3 asynchronous\$2	US-PGPUB; USPAT; EPO	OR	OFF	2006/04/25 18:13

EAST Search History

S16	8	S15 and S3	US-PGPUB; USPAT; EPO	OR	OFF	2006/04/26 10:57
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